# 10/549933 JC17 Rec'd PCT/PTO 20 SEP 2005

### **Application Data Sheet**

### **Application Information**

Application Type::

Regular

Subject Matter::

**National Phase** 

Suggested Group Art Unit::

N/A

CD-ROM or CD-R?::

None

Sequence submission?::

None

Computer Readable Form (CRF)?::

No

Title::

SUBSTRATE PROCESSING APPARATUS

AND SEMICONDUCTOR DEVICE

PRODUCING METHOD

Attorney Docket Number::

2342-0143PUS1

Request for Early Publication?::

No

Request for Non-Publication?::

No

**Total Drawing Sheets:**:

8

Small Entity?::

No

Petition included?::

No

Secrecy Order in Parent Appl.?::

No

#### **Applicant Information**

**Applicant Authority Type::** 

Inventor

Primary Citizenship Country::

Japan

Status::

Full Capacity

Given Name::

Naoharu

Family Name::

**NAKAISO** 

Street of mailing address::

c/o Hitachi Kokusai Electric Inc.

3-14-20, Higashi-Nakano, Nakano-ku

City of mailing address::

Tokyo

Country of mailing address::

Japan

Postal or Zip Code of mailing address::

164-8511

### **Correspondence Information**

Initial 09/20/05 MKM/sII

## 10/549933

### JC17 Rec'd PCT/PTO 20 SEP 2005

Correspondence Customer Number::

02292

**Representative Information** 

Representative Customer Number::

02292

### **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application is a	National Stage of	PCT/JP2004/011266	08/05/04

### **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2003-206526	08/07/03	Yes

### **Assignee Information**

Assignee name::

HITACHI KOKUSAI ELECTRIC INC.

Street of mailing address::

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Country of mailing address::

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Postal or Zip Code of mailing address::

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